

Session Program

17 July 2026

IHL Kickoff meeting

Building the Intelligent Hardware Lab

Universität Siegen – Unteres Schloss Campus, Room: US-C 109
Unteres Schloß 3, 57072 Siegen

Friday 17 July

16:10

Building the Intelligent Hardware Lab

Session |**Location:** Universität Siegen – Unteres Schloss Campus, Room: US-C 109, Unteres Schloß 3, 57072 Siegen

16:10–16:25

Developing the IHL Infrastructure & Ecosystem

16:25–16:45

Technology Vision & Roadmap (ASIC, FPGA, eFPGA & AI Hardware)

16:45–17:20

Round Table: Defining the First IHL Activities

17:20–17:30

Summary, Action Items & Next Steps

17:30–17:35

Closing Remarks

17:35