

Session Program

17 July 2026

IHL Kickoff meeting

Current Expertise & Capabilities

Universität Siegen – Unteres Schloss Campus, Room: US-C 109
Unteres Schloß 3, 57072 Siegen

Friday 17 July

11:10

Current Expertise & Capabilities

Session |**Location:** Universität Siegen – Unteres Schloss Campus, Room: US-C 109, Unteres Schloß 3, 57072 Siegen

11:10–11:20

Analog & Mixed-Signal ASIC Design

Speaker

Tetsuichi Kishishita

11:20–11:30

FPGA Systems & Digital Electronics

Speaker

Michael Karagounis

11:30–11:40

AI-based Real-Time Waveform Triggering

Speaker

Vesselin Dimitrov

11:40–11:55

Cologne Chip: Industrial Partnership and Future Collaboration

Speaker

Michael Gude

11:55–12:05

Discussion: Existing Expertise and Future Opportunities

12:25